

Software Reliability

Lecture 4

Bounded Model Checking for Software

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Hardware verification using SAT

A circuit can be described as a **transition system** in Boolean logic

Transition system can be **unwound** to consider all states of hardware reachable within k transitions

SAT solver can be used to check whether the unwound transition system can reach **bad** states

Known as **bounded model checking**

Look for bugs
up to **bound** k

Model checking term used
mainly for historical reasons

The most widely used technique for hardware verification

Why is BMC successful?

Almost **completely** automatic

Designer needs to express correctness property, that's all

Scales to fairly large designs, due to amazing advances in SAT solving

No abstraction => **no false positives** – great for finding bugs

In some cases can allow full verification

Transition systems

$\vec{\mathbf{x}}$ – vector of Boolean state variables

$\mathbf{I}(\vec{\mathbf{x}})$ – predicate describing initial state of system

$\mathbf{T}(\vec{\mathbf{x}}, \vec{\mathbf{x}}')$ – relation describing transitions between states

$\mathbf{P}(\vec{\mathbf{x}})$ – predicate describing correctness property

Simple example

Boolean state variables: **a, b, c, d**

Initial states:

$$I(\mathbf{a}, \mathbf{b}, \mathbf{c}, \mathbf{d}) = \neg(\mathbf{b} \vee \mathbf{c} \vee \mathbf{d})$$

What are the
initial states?

Transition relation:

$$T((\mathbf{a}, \mathbf{b}, \mathbf{c}, \mathbf{d}), (\mathbf{a}', \mathbf{b}', \mathbf{c}', \mathbf{d}')) = \\ \mathbf{a}' \wedge \mathbf{b}' \Leftrightarrow \mathbf{a} \wedge \mathbf{c}' \Leftrightarrow \mathbf{b} \wedge \mathbf{d}' \Leftrightarrow \mathbf{c}$$

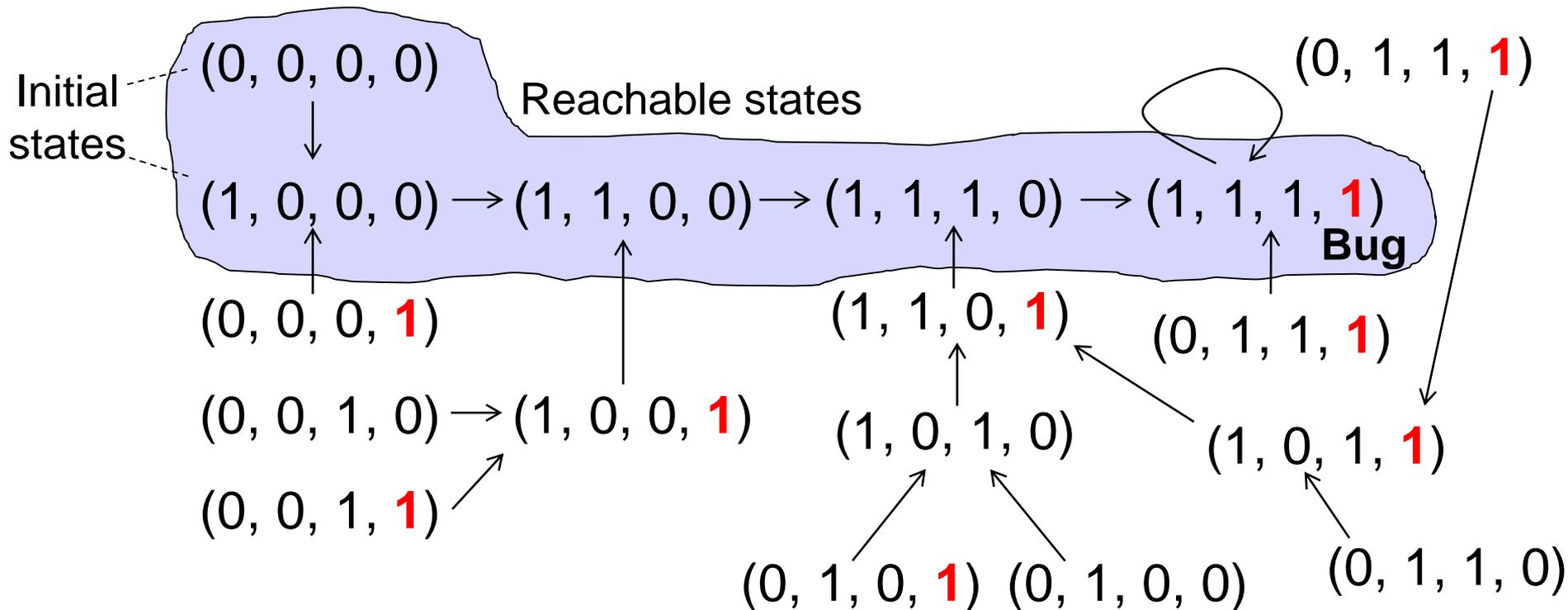
Correctness property:

$$P(\mathbf{a}, \mathbf{b}, \mathbf{c}, \mathbf{d}) = \neg \mathbf{d}$$

Simple example

Is it correct? Let's use 1 and 0 for *true* and *false*

Explore state space:



BMC for transition systems

Given transition system:

$I(\vec{x})$ – initial states

$T(\vec{x}, \vec{x}')$ – transition relation

$P(\vec{x})$ – correctness property

check formula: $I(\vec{x}_0) \ \&\& \ T(\vec{x}_0, \vec{x}_1) \ \&\& \ \dots \ \&\& \ T(\vec{x}_{k-1}, \vec{x}_k)$
 $\ \&\& \ \neg(P(\vec{x}_0) \ \&\& \ \dots \ \&\& \ P(\vec{x}_k))$

Formula requests a sequence of states:

- starting in the initial state
- connected by transitions
- such that at least one state is **bad**

Formula SAT: **P does not hold**

UNSAT: **P** holds along all paths of length $\leq k$

BMC for simple example

$$I(a, b, c, d) = !(b \parallel c \parallel d)$$

$$T((a, b, c, d), (a', b', c', d')) =$$

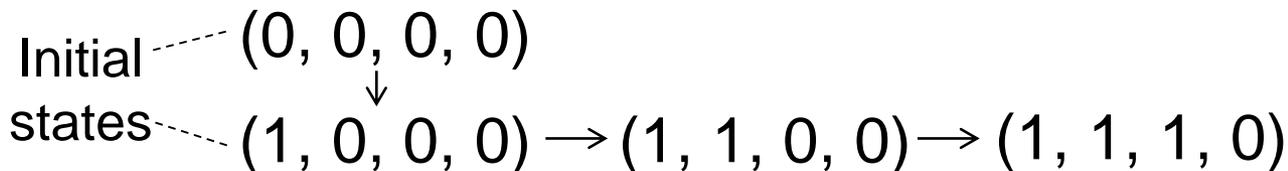
$$a' \ \&\& \ (b' \ \Leftrightarrow \ a) \ \&\& \ (c' \ \Leftrightarrow \ b) \ \&\& \ (d' \ \Leftrightarrow \ c)$$

$$P(a, b, c, d) = !d$$

BMC with $k = 2$

$$\begin{aligned} &!(b_0 \parallel c_0 \parallel d_0) \ \&\& \ (a_1 \ \&\& \ (b_1 \ \Leftrightarrow \ a_0) \ \&\& \ (c_1 \ \Leftrightarrow \ b_0) \ \&\& \ (d_1 \ \Leftrightarrow \ c_0)) \\ &\quad \&\& \ (a_2 \ \&\& \ (b_2 \ \Leftrightarrow \ a_1) \ \&\& \ (c_2 \ \Leftrightarrow \ b_1) \ \&\& \ (d_2 \ \Leftrightarrow \ c_1)) \\ &\quad \&\& \ !(d_0 \ \&\& \ !d_1 \ \&\& \ !d_2) \end{aligned}$$

UNSAT: P holds up to depth 2



BMC for simple example

$$I(a, b, c, d) = !(b \parallel c \parallel d)$$

$$T((a, b, c, d), (a', b', c', d')) =$$

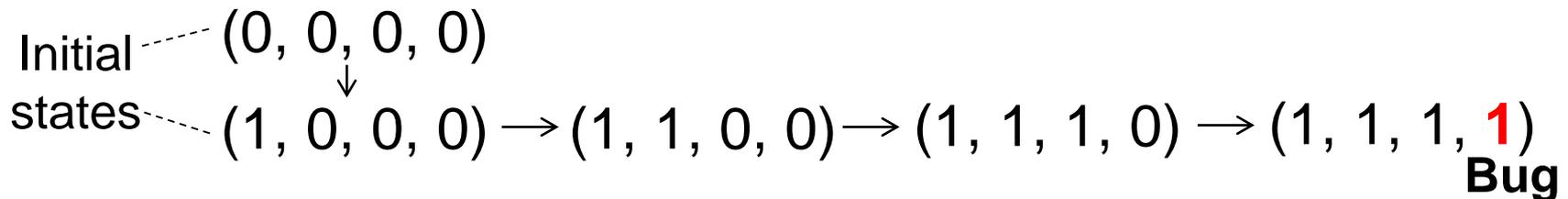
$$a' \ \&\& \ (b' \ \lt;=> \ a) \ \&\& \ (c' \ \lt;=> \ b) \ \&\& \ (d' \ \lt;=> \ c)$$

$$P(a, b, c, d) = !d$$

BMC with $k = 3$

$$\begin{aligned} &!(b_0 \parallel c_0 \parallel d_0) \ \&\& \ (a_1 \ \&\& \ (b_1 \ \lt;=> \ a_0) \ \&\& \ (c_1 \ \lt;=> \ b_0) \ \&\& \ (d_1 \ \lt;=> \ c_0)) \\ &\ \&\& \ (a_2 \ \&\& \ (b_2 \ \lt;=> \ a_1) \ \&\& \ (c_2 \ \lt;=> \ b_1) \ \&\& \ (d_2 \ \lt;=> \ c_1)) \\ &\ \&\& \ (a_3 \ \&\& \ (b_3 \ \lt;=> \ a_2) \ \&\& \ (c_3 \ \lt;=> \ b_2) \ \&\& \ (d_3 \ \lt;=> \ c_2)) \\ &\ \&\& \ (!d_0 \ \&\& \ !d_1 \ \&\& \ !d_2 \ \&\& \ !d_3) \end{aligned}$$

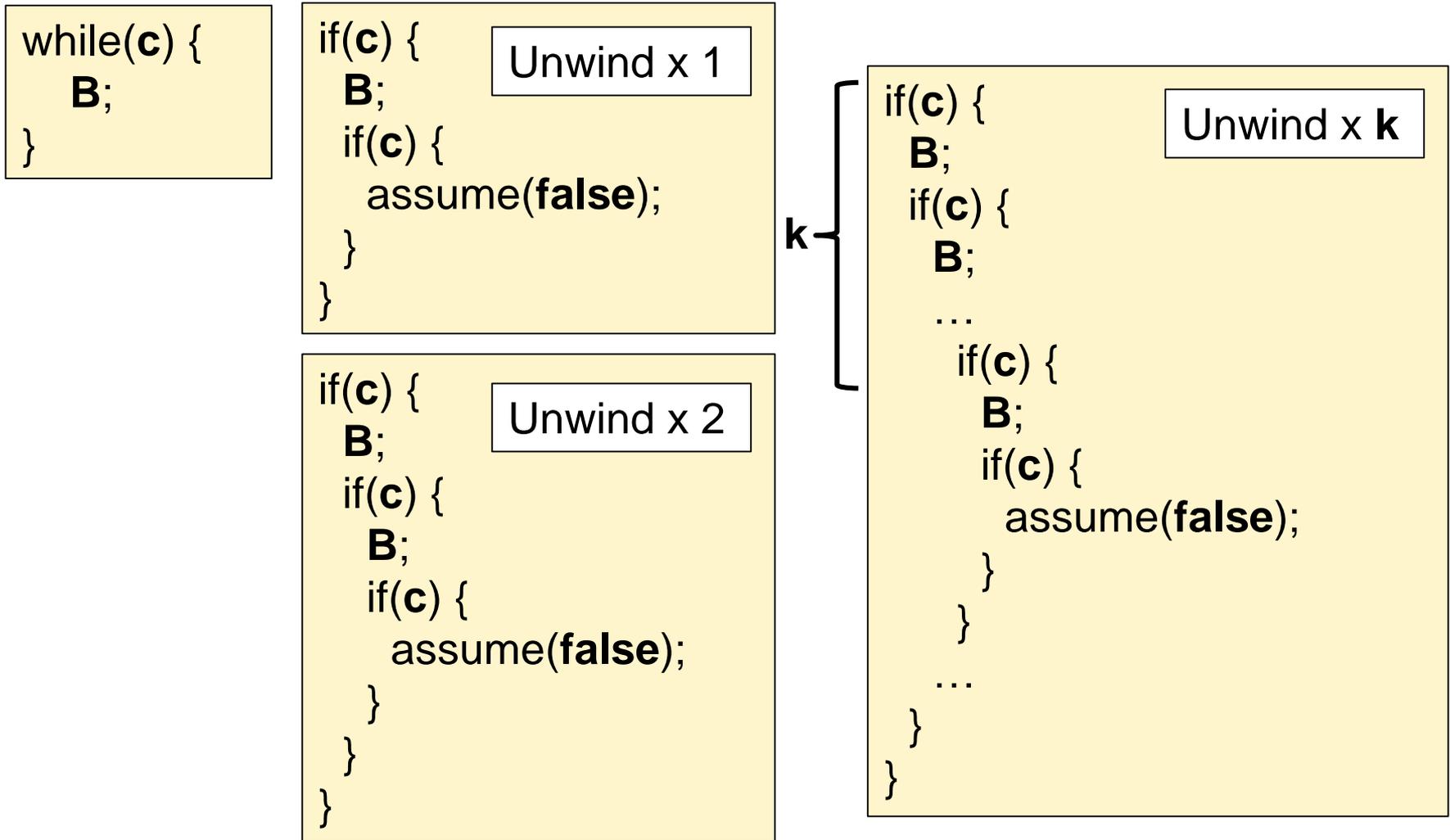
SAT: P **violated** at depth 3



BMC for software

- System-level software: bit-level accuracy is often important (if software does bit-manipulation)
- Idea: represent operations using their circuits
- Either:
 - Apply BMC for transition systems
 - or
 - Directly unwind loops of program
- We shall study the latter approach
- BMC for software is implemented by the CBMC tool (a bounded model checker for C programs) – cprover.org

Loop-based unwinding: program level



Resulting loop-free programs can be analysed using:
predication + SSA renaming + formula generation

Loop-based unwinding and correctness:

P

```
while(c) {  
  B;  
}
```

P'

```
if(c) {  
  B;  
  if(c) {  
    B;  
    ...  
    if(c) {  
      B;  
      if(c) {  
        assume(false);  
      }  
    }  
    ...  
  }  
}
```

Unwind x k

P' incorrect => **P incorrect**

P' correct does not imply
P correct

P' correct tells us that **P**
cannot go wrong **within k**
loop iterations

- **there may be deeper**
bugs

Loop-based unwinding:
under-approximation

Exercises:

Write a simple program that is correct for a small loop unwinding depth but incorrect for a larger loop unwinding depth

We can unwind with $k = 0$. What does this look like?
Can this ever be useful?

Unwinding depth can be chosen loop-by-loop

```
while(c) {  
  B;  
}  
  
while(d) {  
  E;  
}
```

```
if(c) {  
  B; Unwind x 1  
  if(c) {  
    assume(false);  
  }  
}  
  
if(d) {  
  E; Unwind x 2  
  if(d) {  
    E;  
    if(d) {  
      assume(false);  
    }  
  }  
}
```

Unwinding with nested loops

```
while(c) {  
  while(d) {  
    E;  
  }  
}
```

```
if(c) {  
  while(d) {  
    E;   
  }  
  if(c) {  
    while(d) {  
      E;   
    }  
  }  
  if(c) {  
    assume(false);  
  }  
}
```

Unwind x 2

Inner loop gets
duplicated

Unwinding with nested loops

After unwinding an outer loop, we can unwind duplicated inner loops using **different depths** if we wish

```
while(c) {  
  while(d)  
    E;  
}
```

```
if(c) {  
  while(d) {  
    E;  
  }  
  if(c) {  
    while(d) {  
      E;  
    }  
  }  
  if(c) {  
    assume(false);  
  }  
}
```

Unwind x 2

```
if(c) {  
  if(d) {  
    E;  
    if(d) {  
      assume(false);  
    }  
  }  
}  
if(c) {  
  if(d) {  
    E;  
    if(d) {  
      E;  
      if(d) {  
        assume(false);  
      }  
    }  
  }  
  if(c) {  
    assume(false);  
  }  
}
```

Unwind x 1

Unwind x 2

Unwinding with nested loops: complexity

Suppose **S** is a loop-free fragment of code

If we unwind **all** loops **k** times, how many copies of **S** will there be?

```
while(c1) {  
  S;  
}  
while(c2) {  
  S;  
}  
...  
while(cd) {  
  S;  
}
```

$k \times d$

```
while(c1) {  
  while(c2) {  
    ...  
    while(cd) {  
      S;  
    }  
    ...  
  }  
}
```

k^d

Sound BMC: unwinding assertions

In some domains (especially embedded systems) all loops have fixed, relatively small bounds

Makes it possible to **completely** unwind loops to achieve **sound** verification

...but if bounds are not **explicit**, how do we know what it means to **completely** unwind?

Explicit bound

```
while(i < 10) {  
  // Statements that do  
  // not modify i  
  i = i + 1;  
}
```

Implicit bound

```
while(!finished) {  
  // Statements that guarantee  
  // setting finished to true  
  // within 10 iterations  
}
```

Unwinding assertions

```
while(c) {  
  B;  
}
```

```
if(c) {  
  B; Unwind x 1  
  if(c) {  
    assert(false);  
    assume(false);  
  }  
}
```

```
if(c) {  
  B; Unwind x 2  
  if(c) {  
    B;  
    if(c) {  
      assert(false);  
      assume(false);  
    }  
  }  
}
```

Insert check to determine whether we have unwound completely

```
} {  
if(c) {  
  B; Unwind x k  
  if(c) {  
    B;  
    ...  
    if(c) {  
      B;  
      if(c) {  
        assert(false);  
        assume(false);  
      }  
    }  
    ...  
  }  
}
```

Unwinding assertion

An arrow points from the "Unwinding assertion" box to the `assert(false);` line in the nested if statement.

Correctness with unwinding assertion

P

```
while(c) {  
  B;  
}
```

P'

k

```
if(c) {  
  B;  
  if(c) {  
    B;  
    ...  
    if(c) {  
      B;  
      if(c) {  
        assert(false);  
        assume(false);  
      }  
    }  
  }  
  ...  
}
```

Unwind x k
+
unwinding
assertion

Do we
need
both of
these?

P' correct \Rightarrow P correct

**P' incorrect does not
imply P incorrect**

P' incorrect tells us either

- an assertion of **P** can fail (**P** is **incorrect**) **or**
- the unwinding assertion can fail (**unwinding depth was insufficient**)

Note: we can tell which assertion failed; **may** deduce that **P** is incorrect

Is this an **under-** or **over-**approximation?

A formal definition for over- and under-approximation

Let P and P' be programs.

P **over-approximates** P'

(equivalently, P' **under-approximates** P)

if for every pre-condition A and post-condition B we have:

$$\{ A \} P \{ B \} \text{ is valid}$$
$$\Rightarrow$$
$$\{ A \} P' \{ B \} \text{ is valid}$$

A way to think of this:

“ P' can fail in the same or **fewer** ways than P ”

Note: P **over- and under-approximates** P

We can have distinct programs P and P' such that P under-approximates P' and vice-versa. What does this mean?

Over-approximating by adding assertions

Let **P** be a program and **S** a statement appearing in **P**

Let **P'** be identical to **P**, except that **S** is replaced by:

```
    assert(e);  
    S
```

Then **P'** is an over-approximation of **P** according to our definition

Justification: **P** can fail in fewer ways than **P'** because the addition of `assert(e)` gives **P'** an extra opportunity for failure

Over-approximating using `assert(false)`

Let **P** be a program and **S** a statement appearing in **P**

Let **P'** be identical to **P**, except that **S** is replaced by:

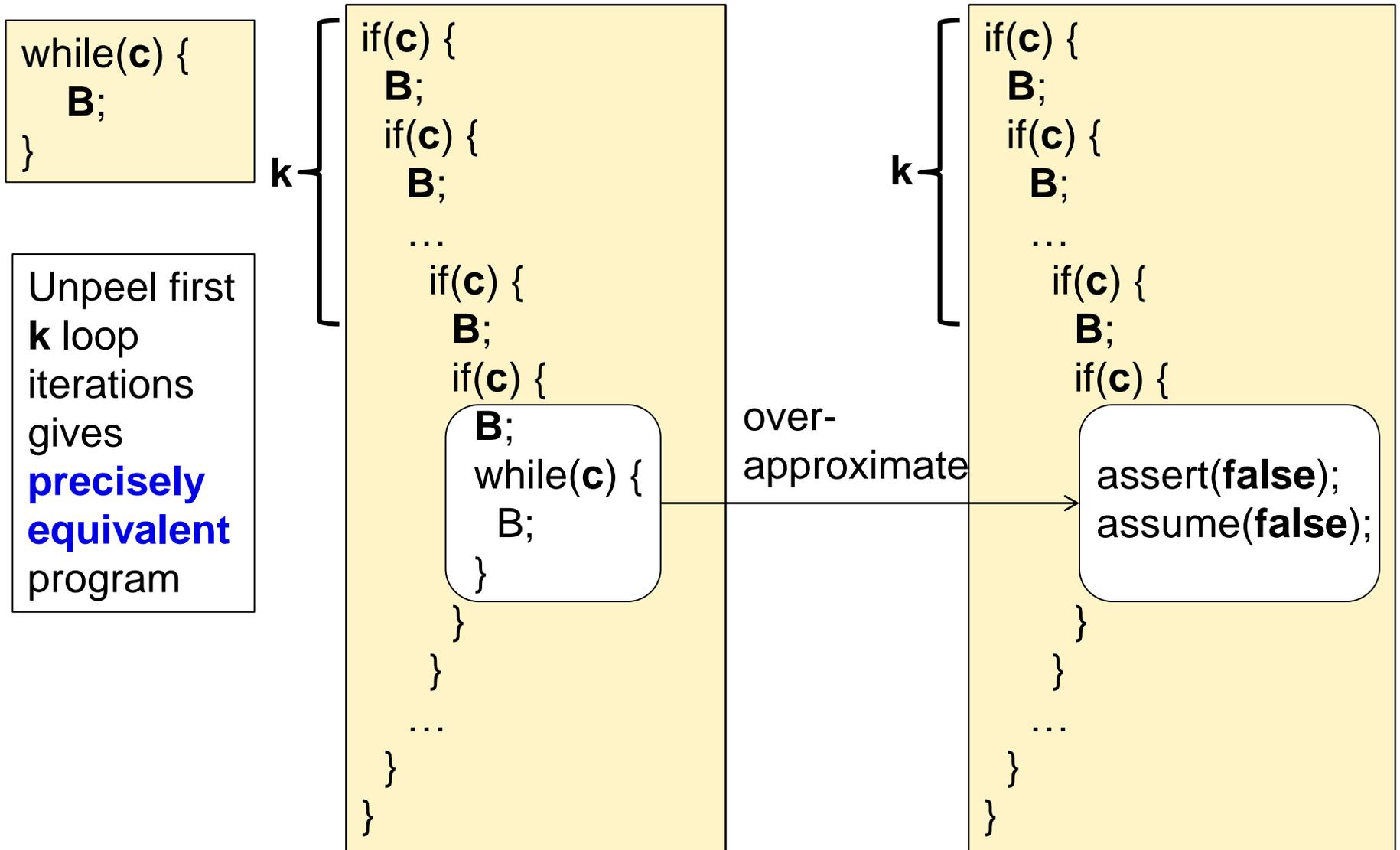
```
    assert(false);  
    T
```

where **T** is any statement

Then **P'** is an over-approximation of **P** according to our definition

Justification: any input to **P** that causes **S** to be executed will cause **P'** to fail. On an input that does not cause **S** to be executed in **P** the programs behave identically

Unwinding assertion gives over-approximation



Summary

Bounded model checking can find bugs in programs

Program is **under-approximated** by unwinding loops up to some user-specified depth

Unwinding assertions can be added to enable verification of programs for which all loops have relatively small upper bounds